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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/061,474
				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2811
				Examiner Name	Belix M Ortiz
Sheet	2	of	2	Attorney Docket Number	SPLX.P0097

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	14.	D. Jongeneel, R. Otten, Y. Watanabe and R. K. Brayton, Area and Search Space Control for Technology Mapping, 37 th Design Automation Conference, 86-91, 2000.	
	15.	Henrik Reif Andersen, An Introduction to Binary Decision Diagrams, October 1997 (minor revisions April 1998), 36 pp.	
	16.	Jerry Burch and David Long, Efficient Boolean Function Matching, Proc. ICCAD 1992, 408-411.	
	17.	John Fishburn, A Depth-Decreasing Heuristic for Combinational Logic; or How to Convert Ripple-Carry Adder into a Carry-Lookahead Adder or Anything In-Between, Proceedings of the 27 th Design Automation Conference, 361-364, 1990.	
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	19.	Kurt Keutzer, DAGON: Technology Binding and Local Optimization by DAG Matching, Proceedings of the 24 th Design Automation Conference, 341-347, 1987	
	20.	Randal Bryant, Symbolic Boolean Manipulation with Ordered Binary Decision Diagrams, CMU CS Tech Report CMU-CS-92-160.	
	21.	Uwe Hinsberger and Reiner Kolla, Boolean Matching for Large Libraries, Proceedings of the 35 th Design Automation Conference, 206-211, June 1998.	
	22.	Yuji Kukimoto, Robert K. Brayton, and Prashant Sawkar, Delay-Optimal Technology Mapping by DAG Covering, Dept. of Electrical Engineering and Computer Science, University of California, Berkeley, Strategic CAD Laboratories, Intel Corp., October 1997.	
	23.	Zbigniew J. Czech, et al., An Optimal Algorithm for Generating Minimal Perfect Hashing Functions, Information Processing Letters, 43(5): 257-264, October 1992	

Examiner Signature	Belix M. Ortiz	Date Considered	12/8/04
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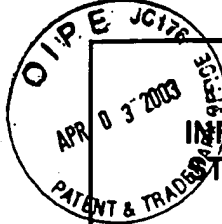
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				Examiner Name	Belus M. Ortiz
Sheet	1	of	2	Attorney Docket Number	SPLX.P0097

RELATED U.S. PATENT APPLICATIONS

Examiner* Initials	Cite No. ¹	U.S. Patent Application		Name of Patentee or Applicant of Cited Document	Date of Filing MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Serial Number				
	1.	10/062,017		Steven Teig & Asmus Hetzel	01-31-02	
	2.	10/061,459		Steven Teig & Asmuz Hetzel	01-31-02	
	3.	10/062,014		Steven Teig & Asmus Hetzel	01-31-02	
	4.	10/062,044		Steven Teig & Asmuz Hetzel	01-31-02	
	5.	10/066,264		Steven Teig & Asmus Hetzel	01-31-02	
bno	6.	10/066,188		Steven Teig & Asmus Hetzel	01-31-02	
	7.	10/062,992		Steven Teig & Asmuz Hetzel	01-31-02	
	8.	10/061,719		Steven Teig & Asmus Hetzel	01-31-02	
	9.	10/066,456		Steven Teig & Asmus Hetzel	01-31-02	
	10.	10/062,993		Steven Teig & Asmus Hetzel	01-31-02	
	11.	10/062,047		Steven Teig & Asmuz Hetzel	01-31-02	

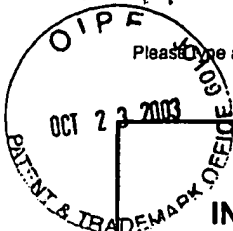
U.S. PATENT DOCUMENTS

Examiner* Initials	Cite No. ¹	U.S. Patent Document Number	Kind Code* (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
bmo	12.	5,649,165		Jain et al.	07-15-97	
bmo	13.	6,301,687	B1	Jain et al.	10-09-01	

Examiner Signature	Belus M. Ortiz	Date Considered	12/8/04
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		Number	Kind Code* (if known)			
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Sheet

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10/061,474

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First Named Inventor

Steven Teig, et al.

Group Art Unit

2811

Examiner Name

Belu M. Orlin

Attorney Docket Number

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U.S. PATENT DOCUMENTS

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bmo	1.	5,519,630	05-21-1996	Nishiyama et al.	<div>RECEIVED</div> <div>DEC 08 2003</div> <div>Technology Center 2100</div>
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NON PATENT LITERATURE DOCUMENTS

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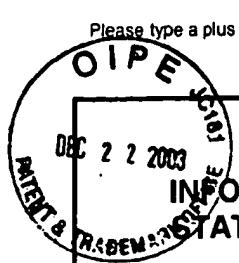
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		Number	Kind Code* (if known)			
<i>BW</i> 	1.	5,521,835		05-28-1996	Trimberger	
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Examiner Signature <i>Belw M Ortiz</i>	Date Considered	12/8/04
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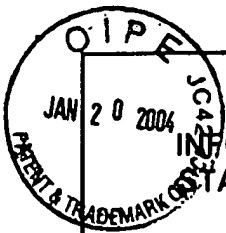
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	1.	2002/0157063	A1	10-24-2002	Besson	
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	4.	4,703,435		10-27-1987	Darringer et al.	
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	15.	6,446,240	B1	09-03-2002	Lyer et al.	
	16.	6,453,447	B1	09-17-2002	Gardner et al.	
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	18.	6,519,609	B1	02-11-2003	Touzet	
	19.	6,539,536	B1	03-25-2003	Singh et al.	
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	21.	6,546,541	B1	04-08-2003	Petranovic et al.	

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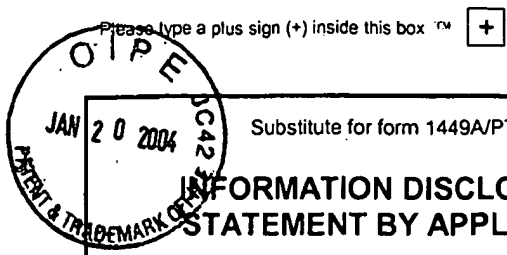
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	24.	NN9411291, "Design of Portable Library using Parameterized Cells", IBM Technical Disclosure Bulletin, Vol. 37, No. 11, pp. 291-292 (4 pages).			
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/061,474
				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2811
				Examiner Name	Belus M. Ortiz
Sheet	1	of	1	Attorney Docket Number	SPLX.P0097

U.S. PATENT DOCUMENTS						
Examiner* Initials	Cite No. ¹	U.S. Patent Document		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
bw	1.	6,519,754	B1	02-11-2003	McElvain et al.	

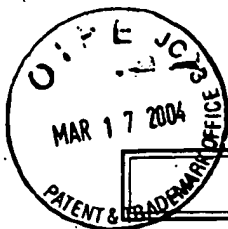
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Examiner Signature	Belus M. Ortiz	Date Considered	12/8/02
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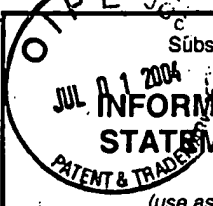
Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Structure for storing a plurality of sub-networks						
Application Number:		10/061474					
Confirmation Number:		3694					
First Named Applicant:		Steven Teig	RECEIVED MAR 19 2004 Technology Center 2100				
Attorney Docket Number:		SPLX.P0097					
Art Unit:		2175					
Examiner:		Dov Popovici					
Search string:		(6216252).pn.					
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
0102	1	6216252	2001-04-10	Dangelo et al.	B1		
Signature							
Examiner Name				Date			
Belix M. Ochi				12/8/04			

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 <p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				Application Number	10/061,474
				Filing Date	1/31/2002
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2175
				Examiner Name	Belix M. Ortiz
Sheet	1	of	1	Attorney Docket Number	SPLX.P0097

NON PATENT LITERATURE DOCUMENTS			
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
brio	1.	Krishnamoorthy et al., Boolean Matching of Sequential Elements, Proceedings of the 31st annual conference on Design automation conference, pp. 691-697, June 06-10, 1994, San Diego, California, United States.	

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Technology Center 2100

Examiner Signature <i>Belix M. Ortiz</i>	Date Considered	<i>12/4/04</i>
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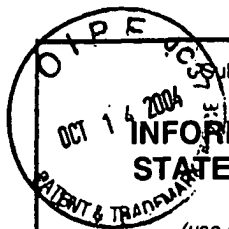
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Filing Date	1/31/2002
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2175
				Examiner Name	Belix M. Ortiz
Sheet	1	of	1	Attorney Docket Number	SPLX.P0097

NON PATENT LITERATURE DOCUMENTS			
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
bmo	1.	DREESSEN J., Standard Cell Development Flow, IEEE, 1990, pp. 450-455	

Examiner Signature <i>Belix M. Ortiz</i>	Date Considered	12/8/04
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